IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	
Filing Date	November 3, 2003
Inventor	
Assignee	Micron Technology, Inc.
Group Art Unit	2815
Examiner	
Attorney Docket No	MI22-2438
Title: Methods of Forming Dielectric Ma	
Semiconductor Substrates	

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

dria, VA 22313-1450 CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8

I hereby certify that, on the date shown below, the following papers are being facsimile transmitted to the U.S. Patent and Trademark Office at facsimile number: (703) 872-9306

- 1. Certificate of Facsimile Transmission
- 2. Supplemental Information Disclosure Statement w/PTO-1449

Dated: January 9,2004

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NUMBER OF PAGES IN FACSIMILE: 3

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References –See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

Citation of the referenced art is respectfully requested.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

By: